

HEART STIMULATION DEVICE

Technical field

The present invention relates to a biventricular stimulation device comprising a pulse generator for delivering stimulation pulses at least to the ventricles of a patient's heart, and an evoked response detector having first and second, independent ventricular sensing channels for ventricular evoked response detection in the ventricles, said pulse generator being controlled to deliver said stimulation pulses to the second ventricle with a VV time delay after stimulation pulse delivery to the first stimulated ventricle which is shorter than an evoked response detection time window following delivery of a stimulation pulse to the first stimulated ventricle.

Background

In the following the ventricle set to be stimulated first is referred to as "the first ventricle", and the ventricle that is set to be stimulated second as "the second ventricle". Even though there is a difference both in stimulation threshold and amplitude of intrinsic signals for the left and the right ventricles, there will be no difference in stimulation strategy if the left or right ventricle is stimulated first. The terms "first" and "second" are only related to the programming of the stimulation device.

In a biventricular pacemaker with a comparatively short VV time delay of less than e.g. 40 msec the evoked response time window, ERW, for the first stimulated ventricle will be interrupted by a stimulation pulse delivered to the second ventricle, i.e. the last stimulated ventricle of the heart. Thus beat-to-beat evoked response, ER, detection on the first ventricle is impossible with such a short VV time delay.

However, in some cases ER detection is possible on the first ventricle regardless of a short VV time delay as mentioned above.

The purpose of the present invention is to utilize this possibility for providing a biventricular stimulation device with an improved possibility to ER detection in the first ventricle despite a short VV time delay.

Disclosure of the invention

This purpose is obtained by a biventricular stimulation device of the kind defined in the introductory portion and having the characterizing features of claim 1.

5 For a normal stimulation pattern in biventricular stimulation devices the left ventricle is the first stimulated heart chamber and the right ventricle the second one, since LBBB is much more frequent than RBBB. An intrinsic R wave originating from e.g. a conducted P wave will thus be sensed in the right ventricle, probably shortly after the stimulation pulse is delivered to the left ventricle. In the
10 stimulation device according to the invention an ERW is always started after each one of the stimulation pulses to the first ventricle even when a short VV time delays is programmed. The evoked response detector of the stimulation device is then arranged to close the ERW or discard detections therein in response to the emission of a stimulation pulse to the second ventricle during the ERW of the first
15 stimulated ventricle. Thus, only if there is emitted a stimulation pulse to the second ventricle no decision concerning capture or loss of capture of the first ventricle will be taken. An important feature of the stimulation device according to the invention is that the evoked response detector is provided with first and second, independent ventricular sensing channels for ventricular evoked response
20 detection in the respective ventricles, as in for instance Epic HF.

According to advantageous embodiments of the device according to the invention an inhibiting means is provided for inhibiting stimulation in the second ventricle in response to the detection of a sensed intrinsic cardiac event therein, and the VV time delay is less than 40 msec, preferably in the range of 10-30 msec,
25 and the duration of ERW is in the range of 40-100 msec.

Brief description of the drawings

To further explain the invention an embodiment of the device according to the invention will be described in greater detail with reference to the accompanying
30 drawings on which figure 1 illustrates typical placements in the heart of the leads of a stimulation device according to the invention having also an atrial lead implanted, figure 2 is a block diagram of the main electronic circuitry units of a stimulation device like the pacemaker shown in figure 1, figure 3 is a block diagram illustrating the input/output stage in figure 2 in greater detail, figure 4 is a

flow diagram illustrating the function of the embodiment described in connection with the preceding figures, and figure 5 is a timing diagram further illustrating the function of the described embodiment of the device according to the invention.

5 **Description of a preferred embodiment**

Figure 1 shows a heart stimulation device in the form of a pacemaker 10 having an atrial 20 and two ventricular leads 24, 30 for stimulating and independently sensing in the left atrium and the ventricles of the heart 12. The figure shows typical positions for an atrial electrode 22, right ventricular electrodes 32, 34
10 and a left ventricular electrode 26 placed in the coronary sinus. The biventricular pacing lead configuration comprises an unipolar left ventricular lead 24, 26 and a bipolar right ventricular lead 30, 32, 34. By implanting an atrial lead 20, 22 AV synchronous pacing modes are also possible.

Figure 2 is an electronic circuitry block diagram of the main units of the
15 pacemaker 10 in figure 1. The implanted leads 20, 24, 30 in figure are connected to an input/output stage 40 in the pacemaker 10. The stage 40 comprises a pacing and a sensing module as will be further described in connection with figure 3. The operation on the pacemaker is controlled by a CPU 42. The pacemaker also includes a memory 44 for storing information about e.g. how the stimulation
20 threshold has changed over time, how much stimulation has been given, energy consumption etc. which is read out at follow ups. Telemetry means 46 are provided for the communication between the implanted pacemaker and an external programmer.

The input/output stage 40 to which the leads 20, 24 and 30 are connected
25 is shown in a larger scale in figure 3. Thus this stage 40 comprises three channels, one lead 20, 24, and 30 respectively being connected to each of the channels. Each channel comprises a pacing module and a sensing module, and each pacing module includes a pacing stage 48, 50, 52 connected to an amplifier 54, 56, 58. The leads 20, 24, 30 are connected to the outputs of the amplifiers 54, 56, 58 for
30 delivering stimulation pulses to the patient's heart as controlled from the CPU 42.

The leads 20, 24, 30 are also used for sensing signals in the heart and can be connected, via switches 60, 62, 64 to the sensing module of each channel. Each sensing module includes an amplifier 66, 68, 70 in which sensed signals are

amplified and the amplified signals are then supplied to an event detector 72, 74, 76 and ER detector 78, 80, 82 for detecting possible evoked response of the heart. It is important that the three channels for stimulation and sensing are separated.

The switches 60, 62, 64 are controlled to disconnect all sensing modules
5 whenever a stimulation pulse is delivered on anyone of the channels to avoid that the stimulation give rise to disturbances and saturation in the sensing modules. The switches 60, 62, 64 are controlled to otherwise connect the sensing modules to their respective lead 20, 24, 30.

The CPU 42 comprises an inhibiting means for inhibiting stimulation in the
10 second ventricle, e.g. the right ventricle, in response to the detection by the associated sensing module of a sensed cardiac event therein within the VV time delay. The evoked response detector is arranged to close the evoked response detection time window of the first ventricle or discard detections therein in response to the emission of a stimulation pulse to the second ventricle, during the
15 evoked response detection time window of the first stimulated ventricle.

The operation of the above embodiment of the stimulation device according to the invention is illustrated by a flow diagram in figure 4. Thus a command is given triggering stimulation of the first ventricle V1. Collection of data for evoked response, ER, detection in the first ventricle is started, block 84. If no
20 cardiac event, like an R wave, R2, is detected in the second ventricle within the predetermined VV time delay, blocks 86 and 88, a stimulation pulse is delivered to the second ventricle V2, block 90. The connection of ER 1 data is then aborted, block 92, and possible resulting evoked response ER 2 is evaluated and the main procedure is continued.

25 If a cardiac event in the second ventricle, R2, is detected before the expire of the VV time delay stimulation to the second ventricle, V2, is inhibited by the inhibiting means, block 94. If the collection of evoked response data ER1 from the first ventricle is then completed, block 96, these data are evaluated for detection of possible evoked response, block 98, otherwise this ER1 data collection is
30 repeated or continued till completion, cf. block 96. After terminated evoked response detection in the first ventricle the main procedure is repeated.

An example of the operation of the device according to the invention is also illustrated by a timing diagram in figure 5. Line A relates to the atrium and lines LV and RV to the left and right ventricles respectively. P-waves are shown at

100, 102. The left ventricle LV is first stimulated at 104 and an evoked response detection time window ERW 106 is following this stimulation pulse 104. If a stimulation pulse 107 is delivered to the second ventricle RV the ERW of the first ventricle 106 will be closed and the results discarded. No decision concerning capture or loss of capture will then be taken.

The stimulation pulse 107 to the right second ventricle is followed by an ERW 108 for detection of capture or loss of capture in normal way.

If an intrinsic R wave 110 is detected in the second ventricle RV, e.g. originating from a conducted P wave 102, this intrinsic R wave 110 will be sensed in the second ventricle RV, probably shortly after the stimulation pulse in the first ventricle LV. The sensed R wave 110 in the second ventricle RV will then control the inhibiting means to inhibit the second stimulation pulse 114 and ER detection in the associated ERW 112 of the first ventricle LV is performed. Thus, to sum up, if a RV stimulation pulse is emitted during the LV evoked response detection time window no ER detection is performed in LV, else ER detection will always be performed in LV.

The example above relates to patients for which the LV stimulation is programmed to come first, e.g. most LBBB patients. For most RBBB patients the situation will be analogous with first and second ventricles shifted